

10/091840



03/06/02

## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10091840	03/06/2002	380	781	2181	Albert Deady

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714

2133

KEEVEROS

\*\*CONTINUING DATA VERIFIED

NONE

Jr 8/4/2004

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-196223 06/28/2001

JAPAN 2001-66573 03/09/2001

Jr 8/4/2004

PG-PUB DO NOT PUBLISH ☐RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

Jr 8/4/2004

ATTORNEY DOCKET NO

JP920010049US1

TITLE : Decoding circuit, and decoder, decoding method and semiconductor device that use the decoding circuit

U.S. DEPT. OF COMM./PAT &amp; TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheet Drwg.	Figs.Drwg. Print Fig.
Primary Examiner		Application Examiner	
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